

PATENT

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of: Youji TERAUCHI

Conf.:

Appl. No.:

Group:

Filed: January 29, 2004

Examiner:

Title: SEMICONDUCTOR DEVICE AND TESTING CIRCUIT
WHICH CAN CARRIES OUT A VERIFYING TEST
EFFECTIVELY FOR NON VOLATILE MEMORY CELLS

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 29, 2004

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 14 of this paper.

An **Appendix** is attached following the signature page of this paper.